

PATENT NUMBER

178618160

10/21/90

ISSUE CLASSIFICATION	
Class	Subclass

<p>O.I.P.E.</p> <p>SCANNED <u>HKM²</u> G.A. <u>LA</u></p>	<p>PATENT DATE</p>
--	--------------------

APPLICATION NO. 09/879841	CONT/PRIOR	CLASS 7 10	SUBCLASS 2	ART UNIT 3 81	EXAMINER DO
------------------------------	------------	-------------------------------------	---------------	--	----------------

APPLICANTS

Alexander Podkolzin
Valery Kudryavtsev

TITLE

Method and apparatus for optimizing the timing of integrated circuits

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

(FACE)